		Application/Control No. 10/807,678	Applicant(s)/Patent Under Reexamination GIL ET AL.	
Notice of References Cited		Examiner Todd Ingberg	Art Unit 2193	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,535,391	07-1996	Hejlsberg et al.	717/140
*	B	US-6,507,946	01-2003	Alexander et al.	717/145
*	C	US-6,754,887	06-2004	Gil et al.	717/116
*	D	US-6,161,217	12-2000	Detlefs et al.	717/141
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"Space and Time Efficient Memory Layout for Multiiple Inheritance", Joseph Gil et al, IBM T.J. Watson research Center, ACM, pages 256-275
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.